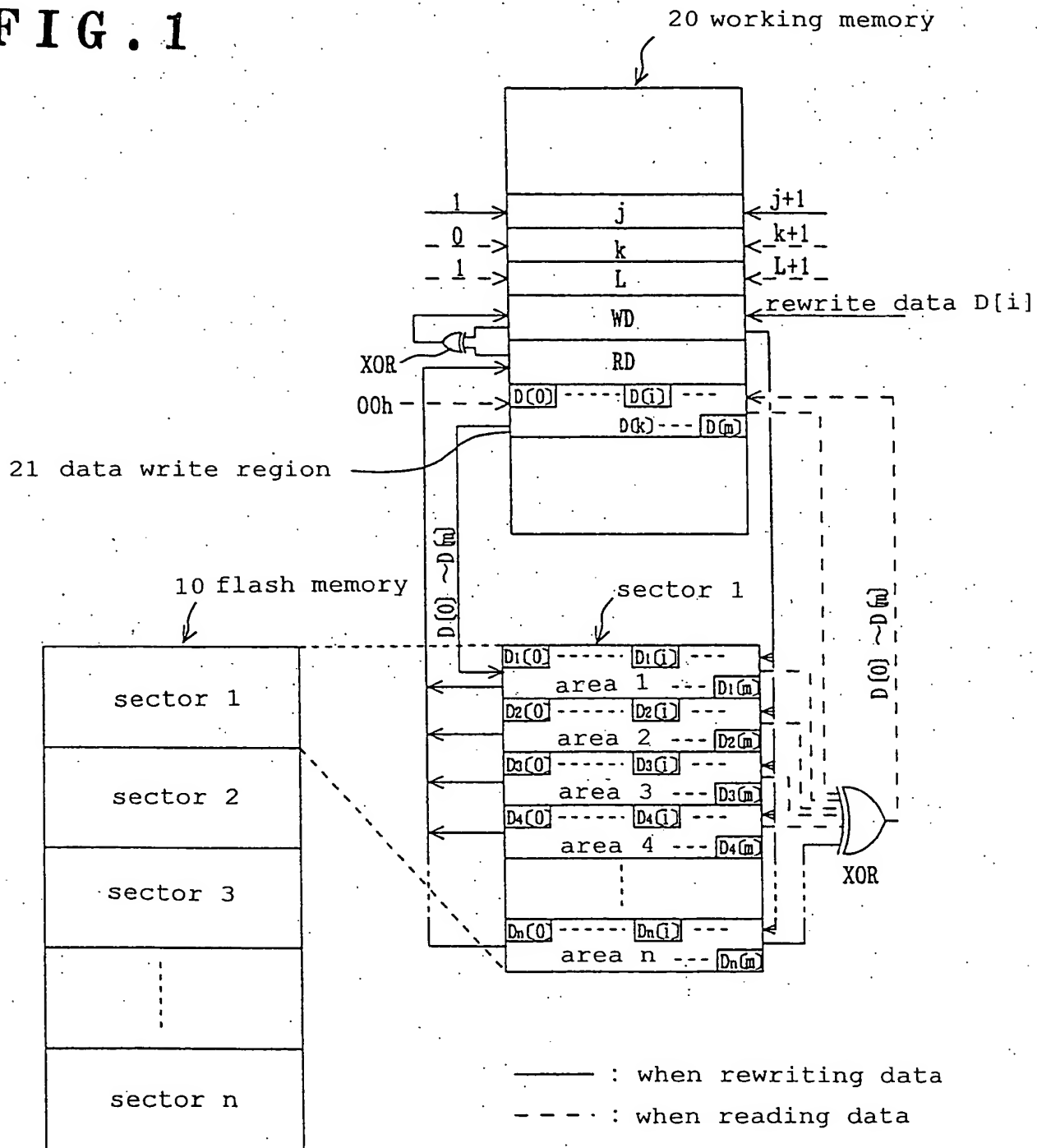


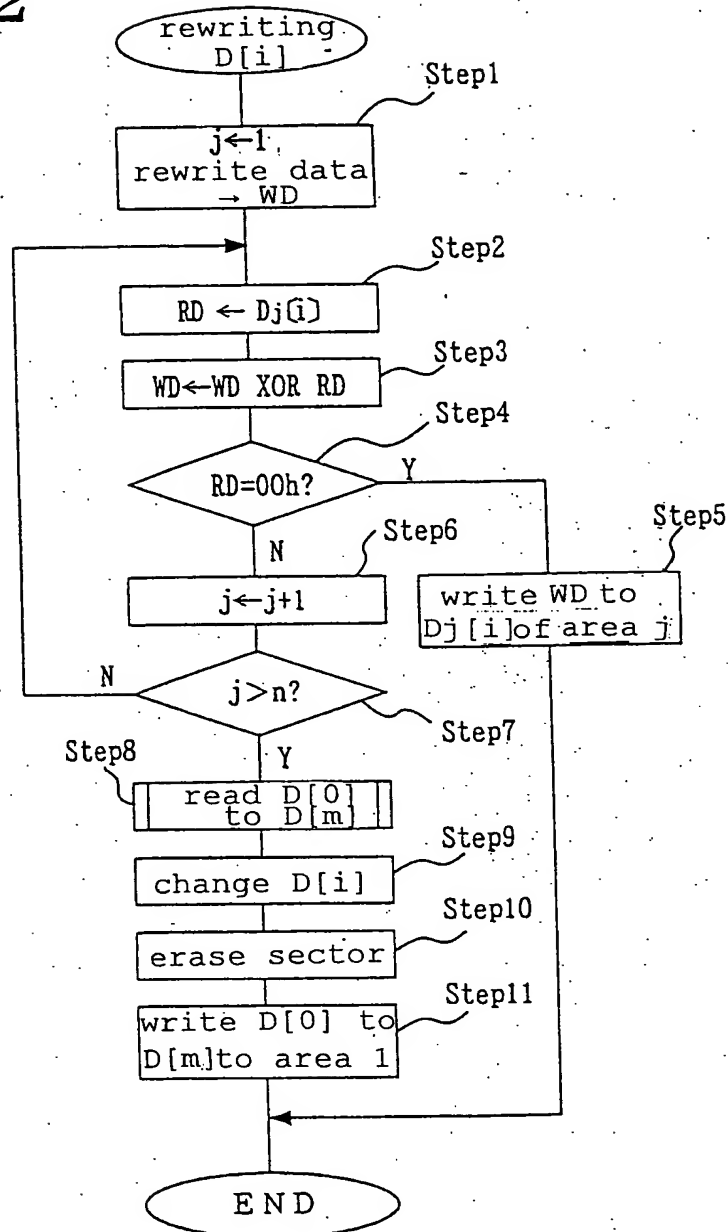
FIG. 1



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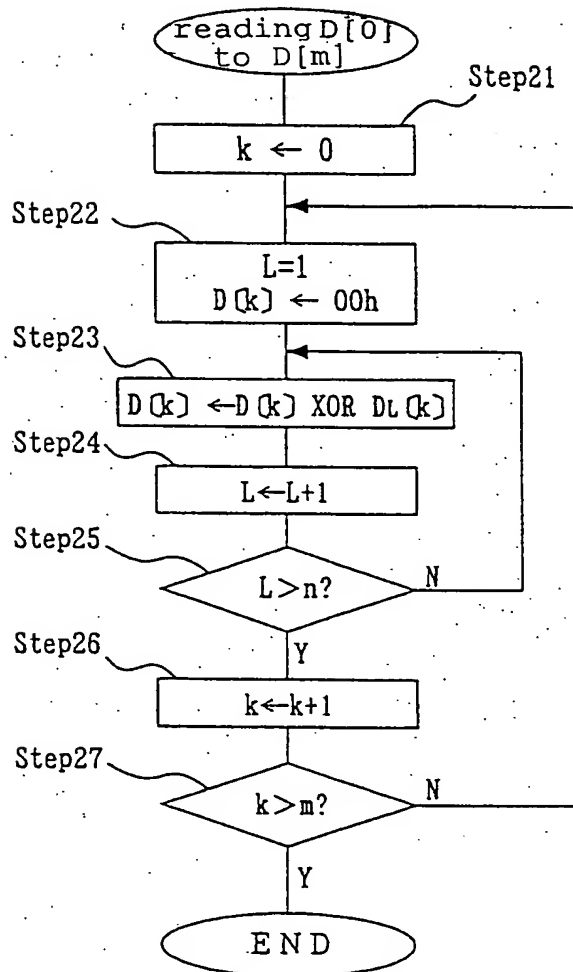
FIG. 2



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FIG. 3



sector 1

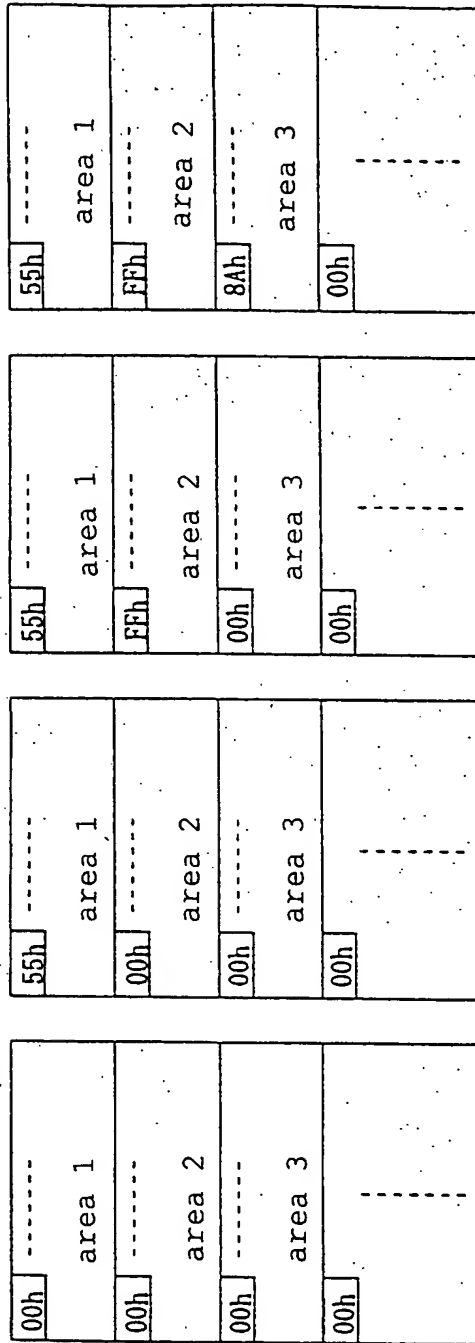
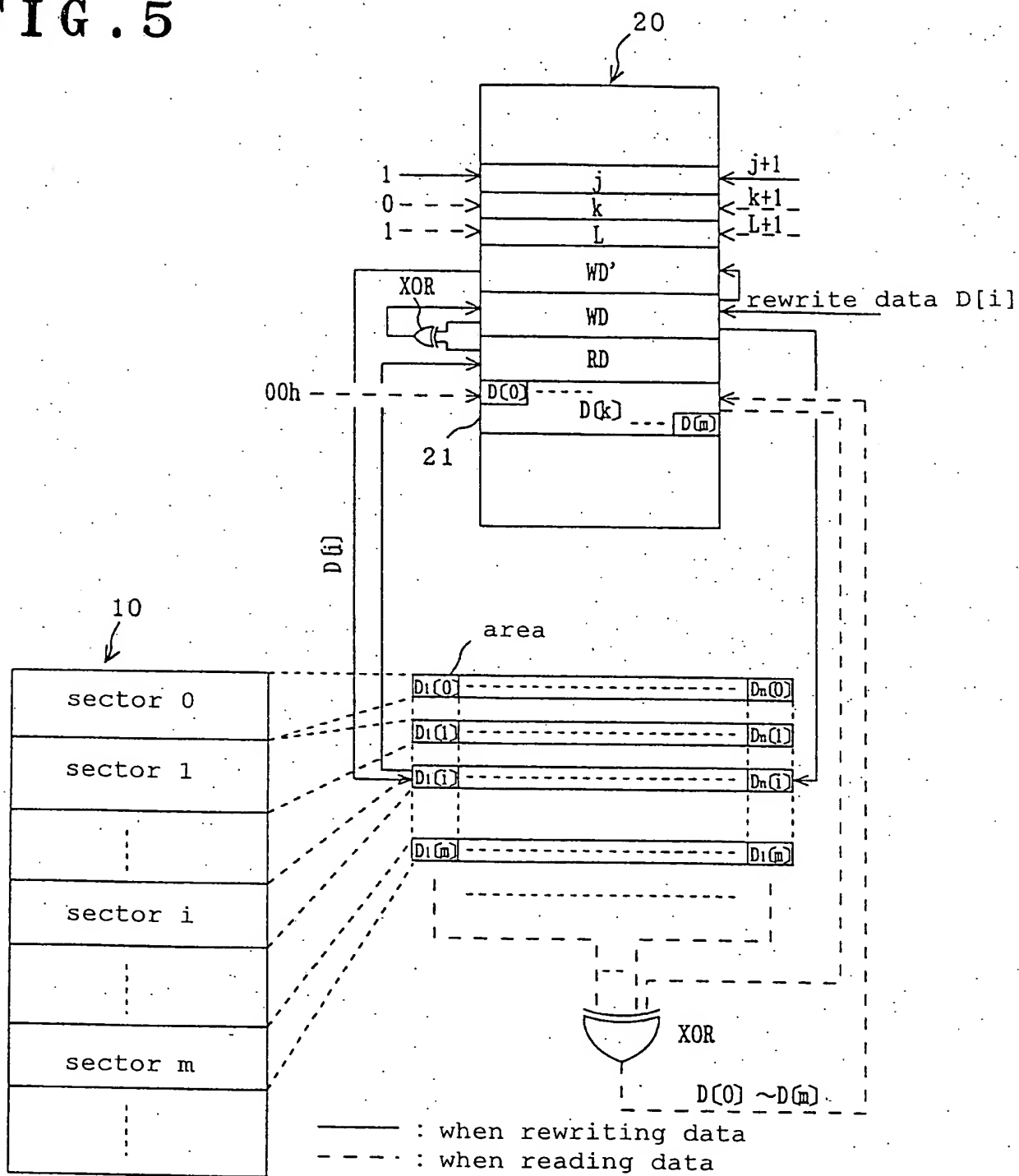


FIG. 4A FIG. 4B FIG. 4C FIG. 4D

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FIG. 5



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FIG. 6

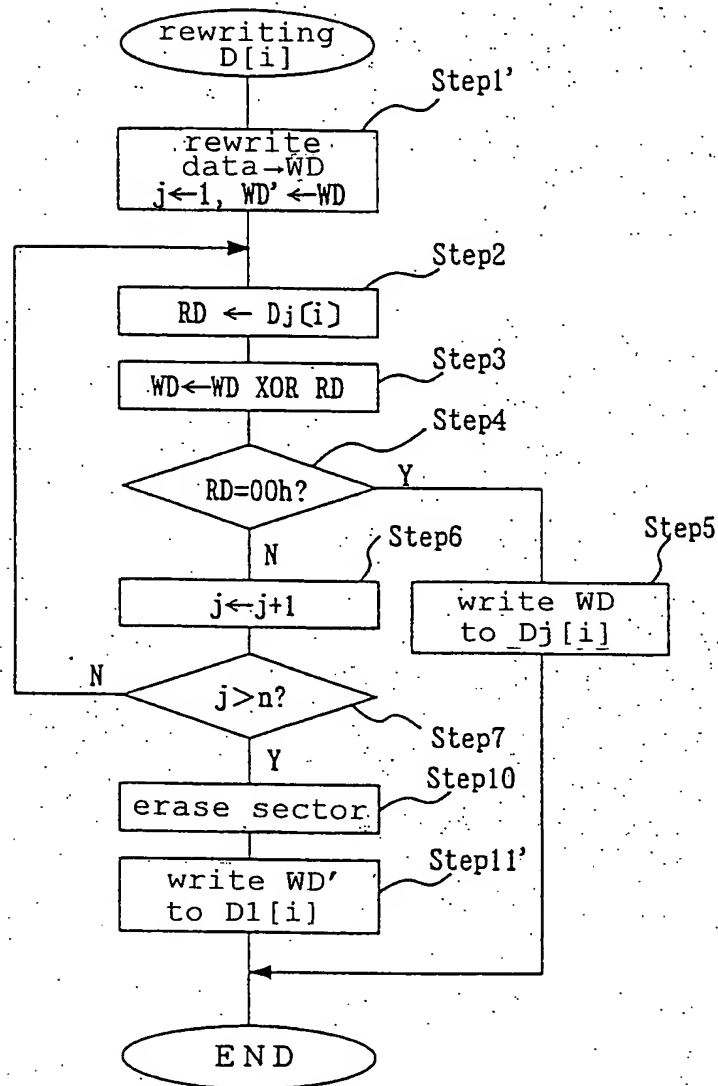
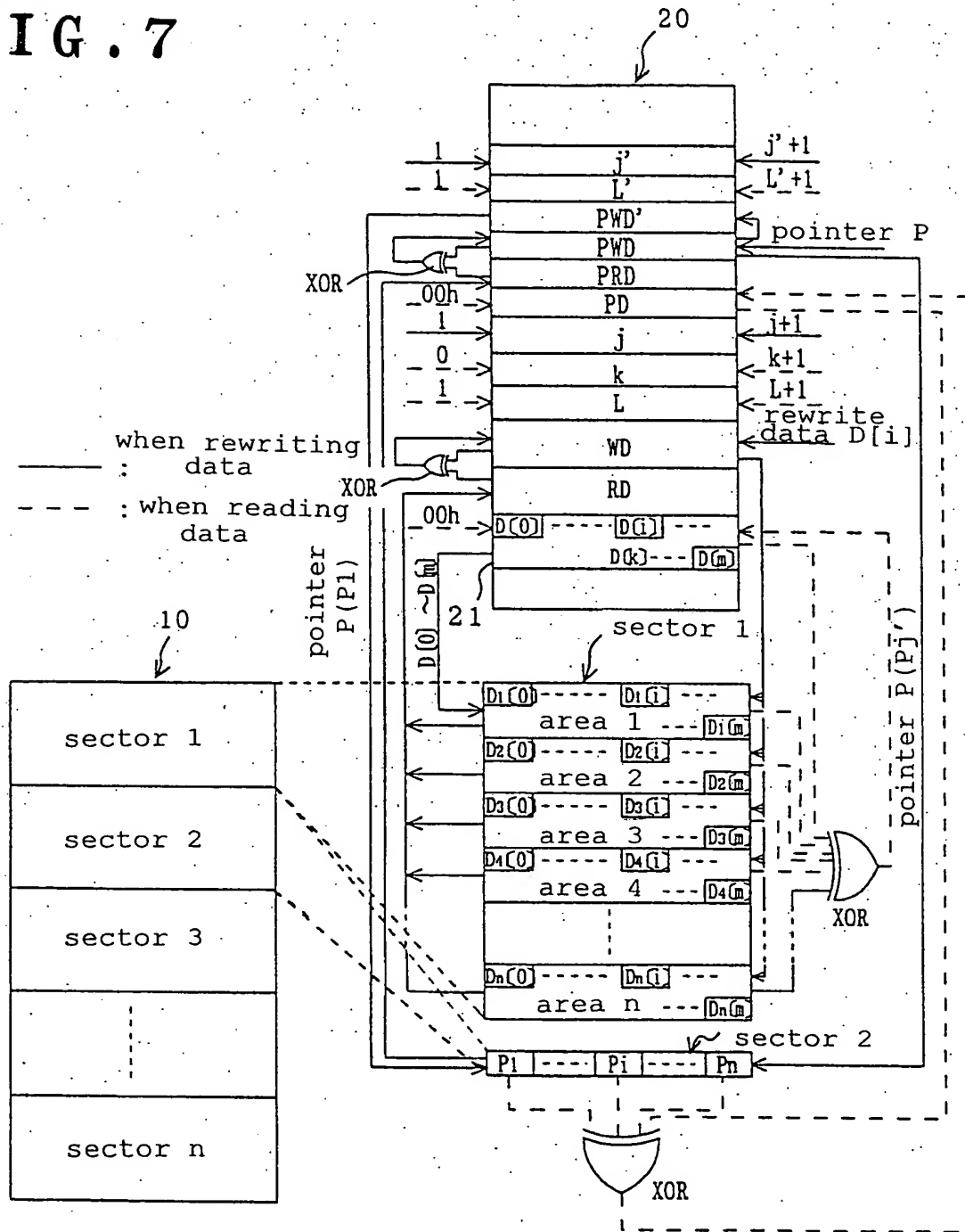


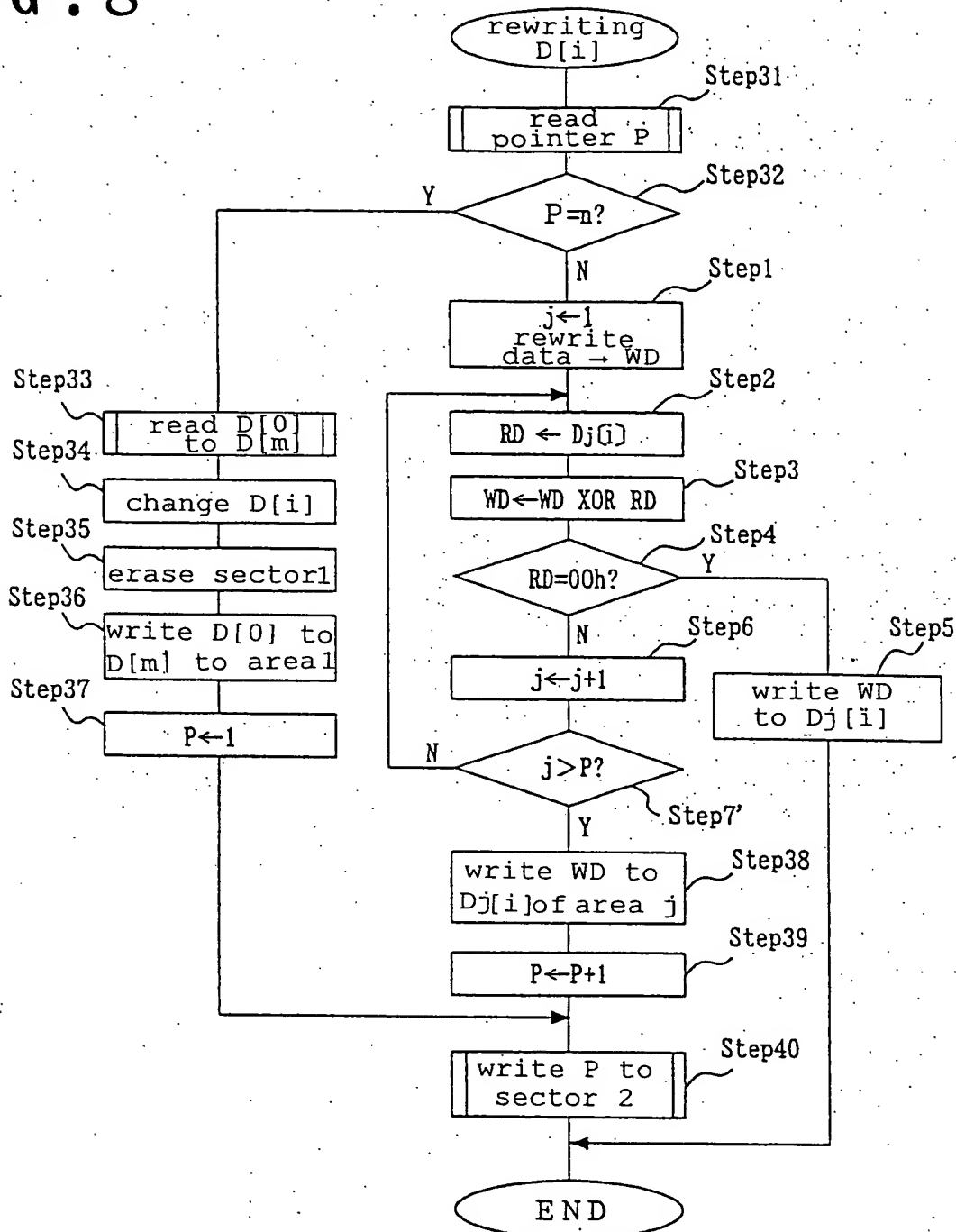
FIG. 7



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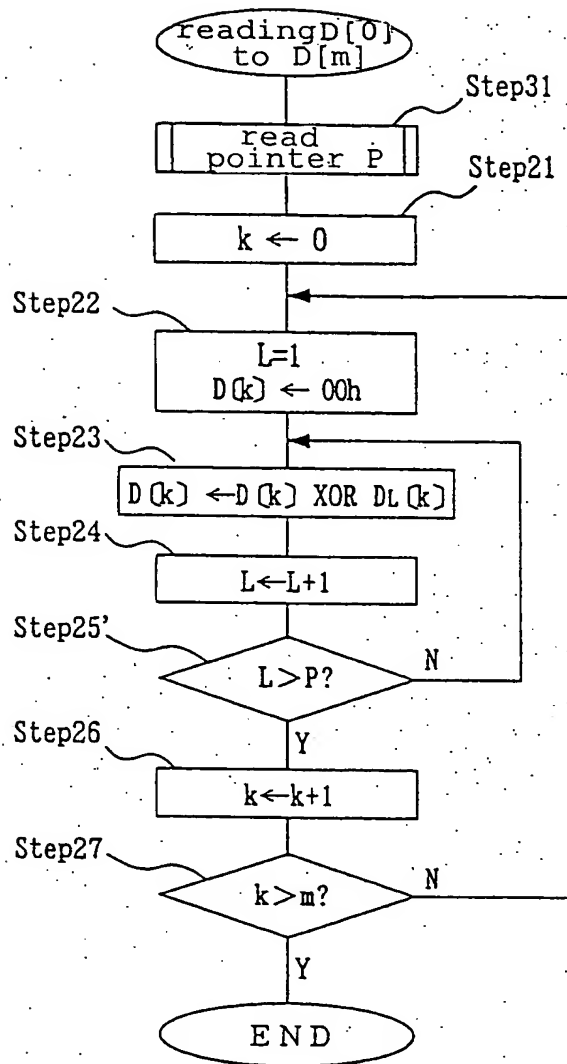
FIG. 8



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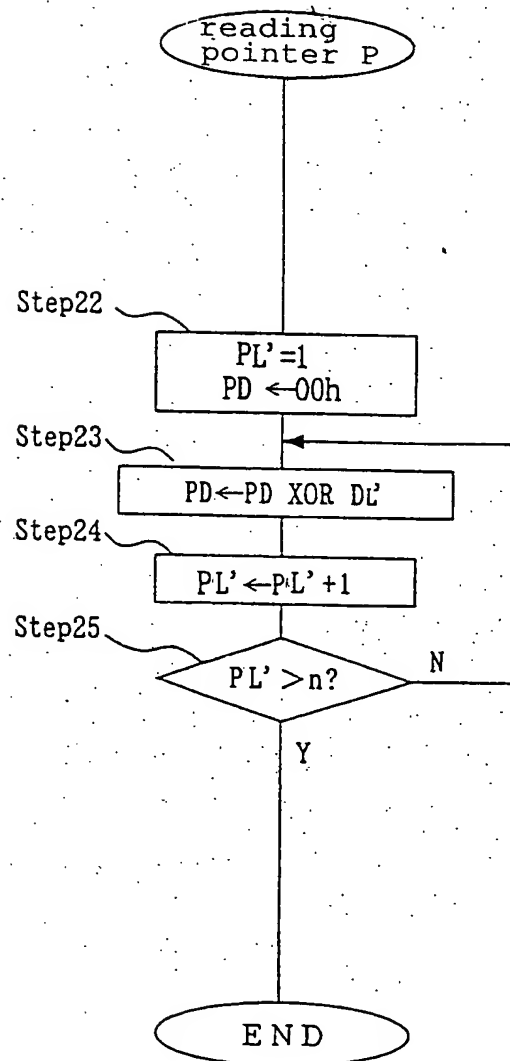
FIG. 9



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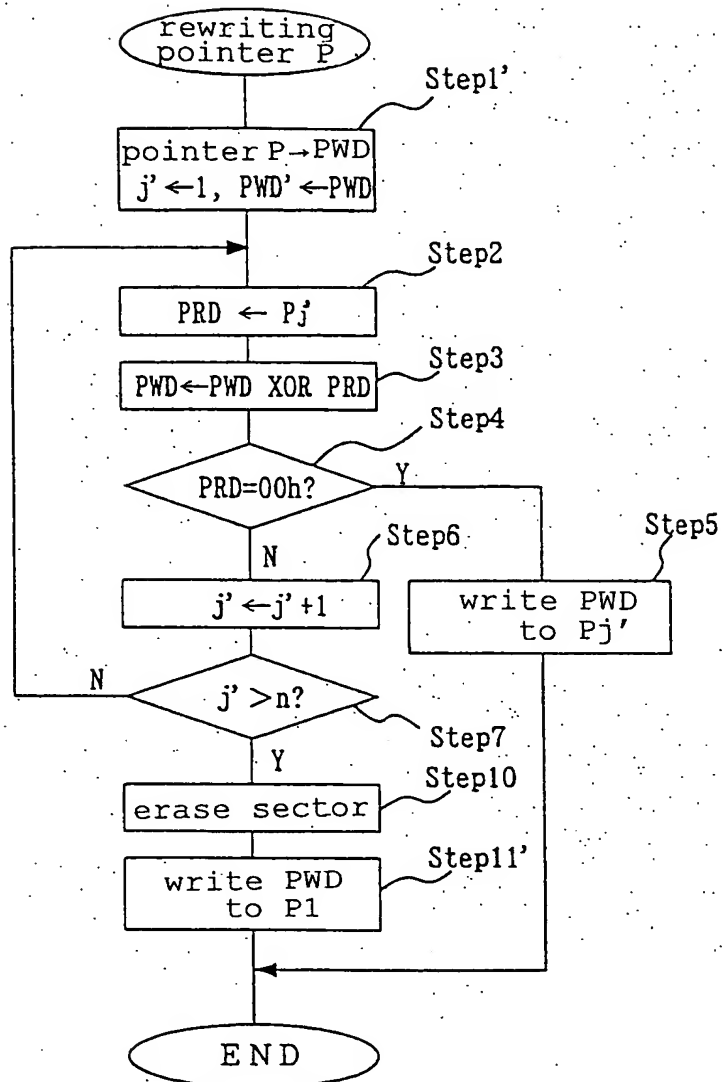
FIG. 10



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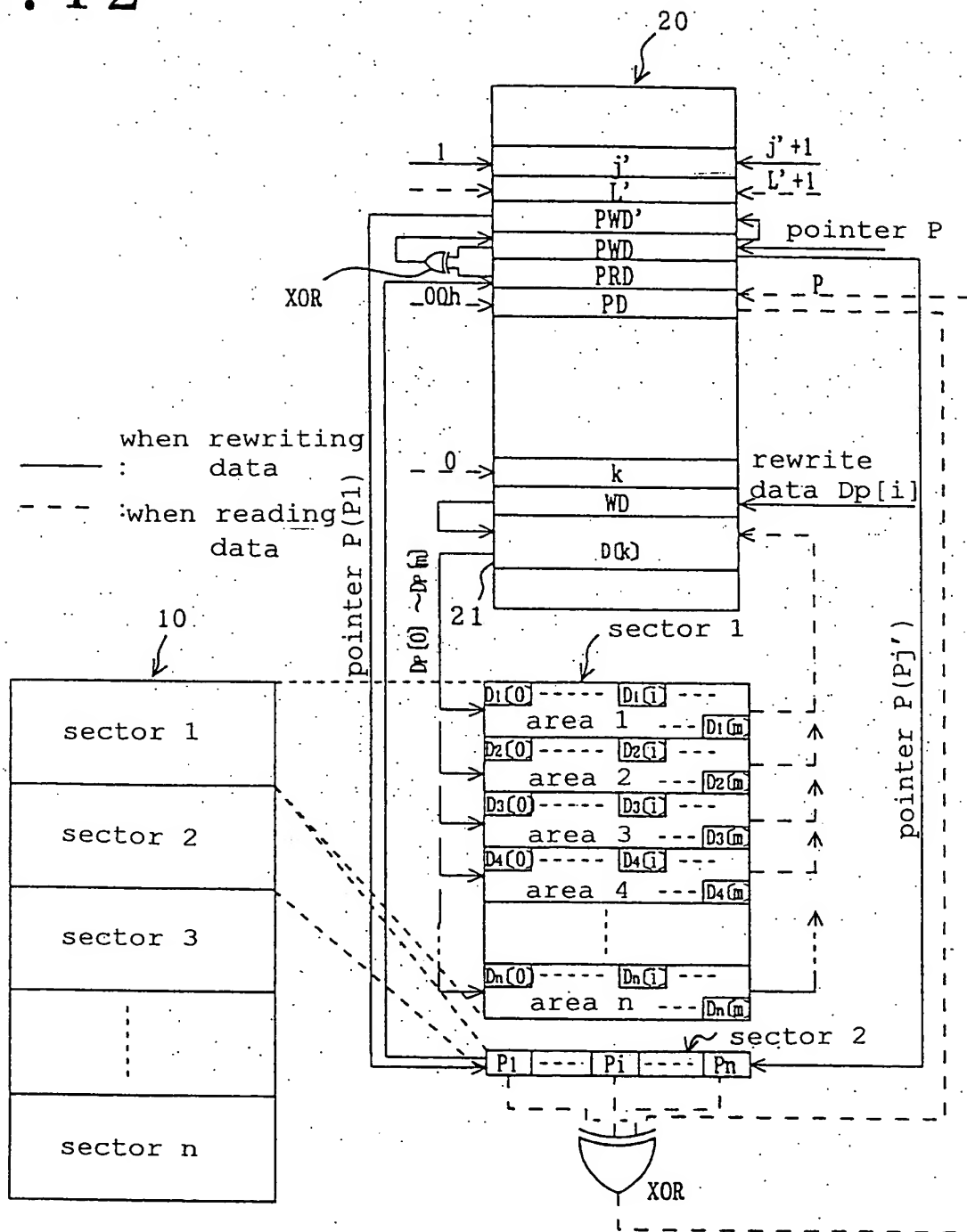
FIG. 11



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FIG. 12



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FIG. 13

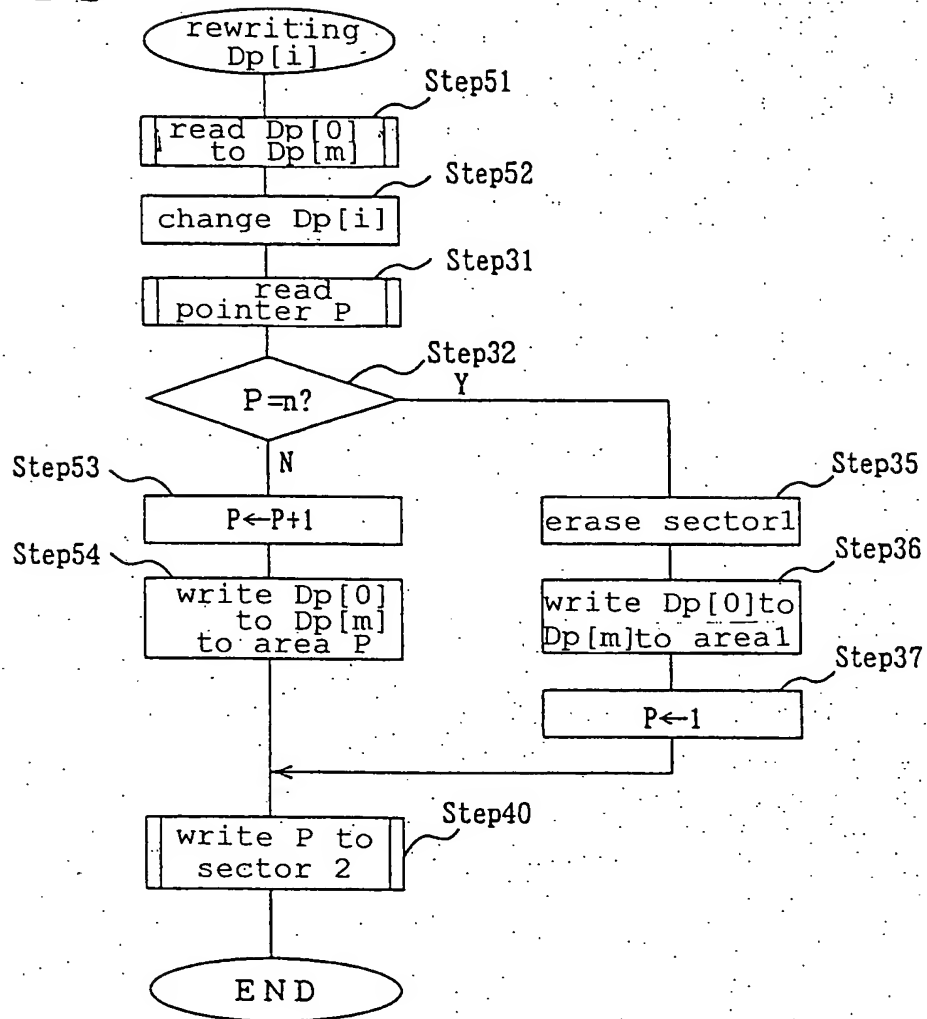


FIG. 14

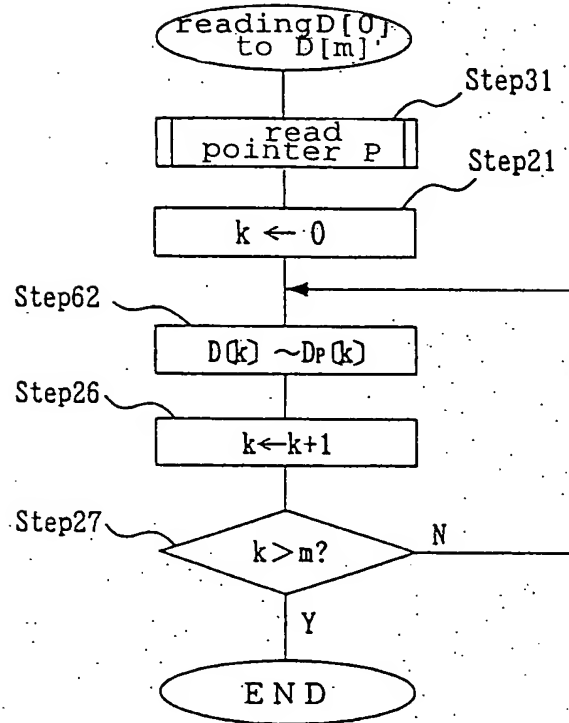


FIG. 15

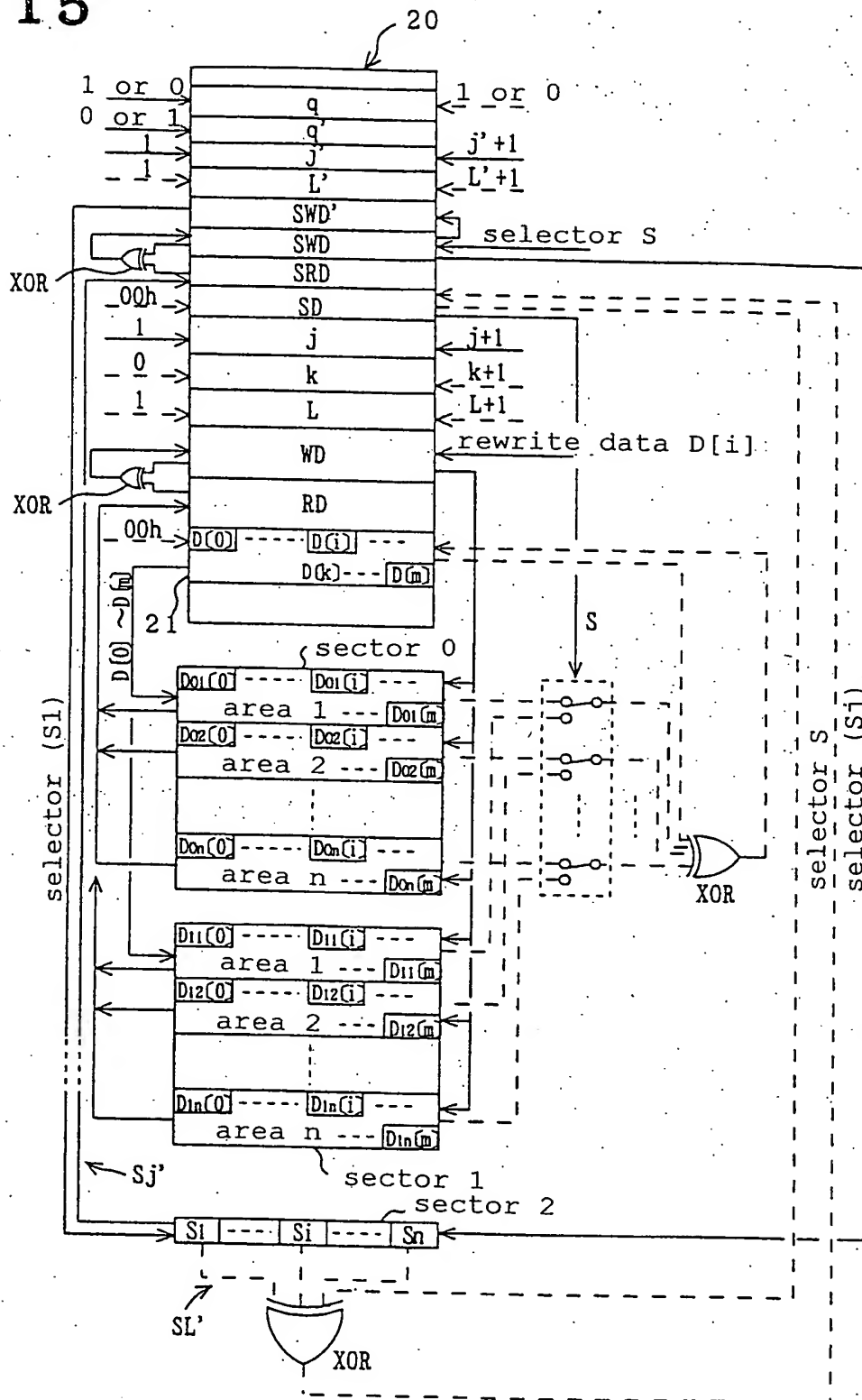


FIG. 16

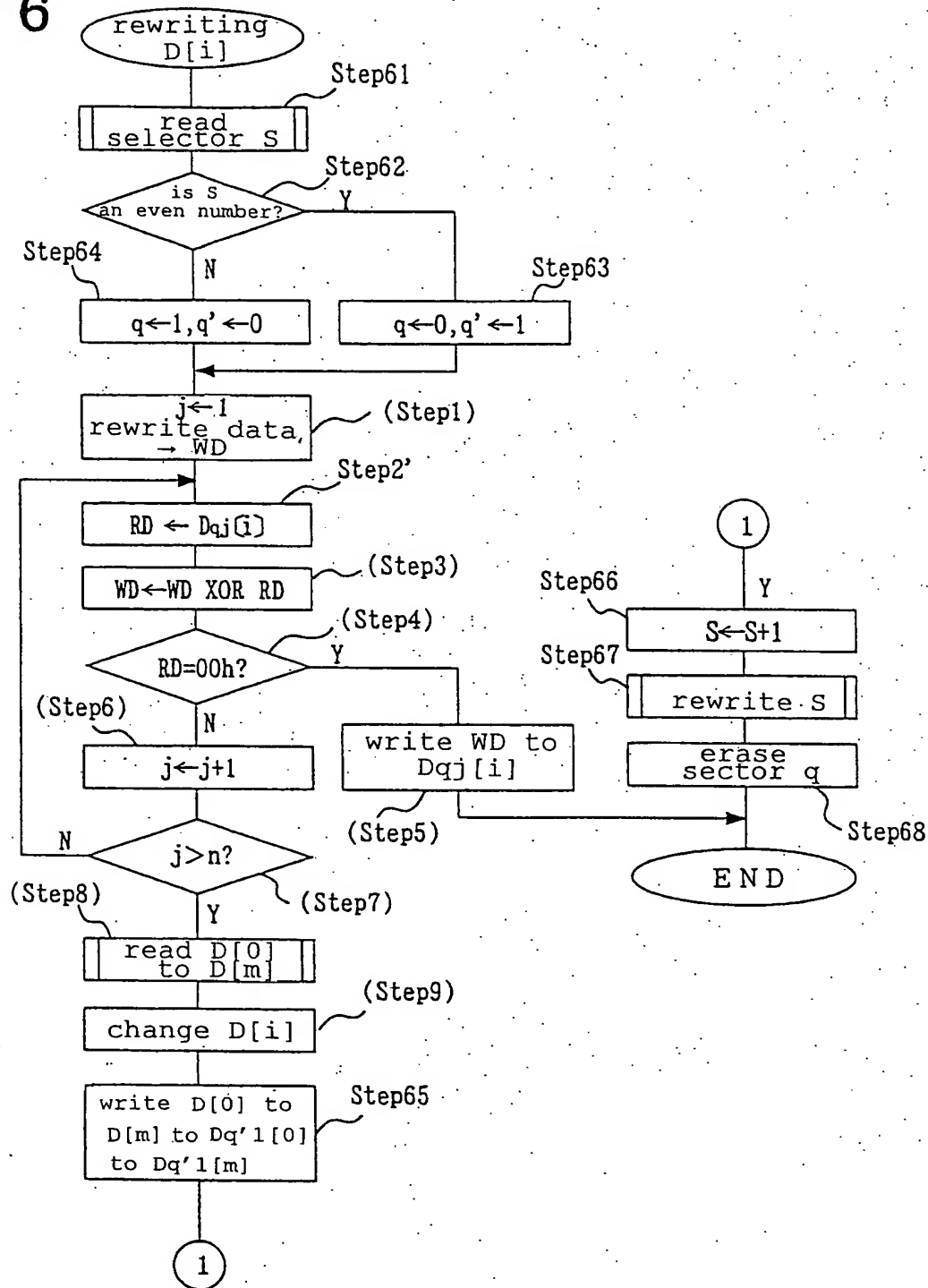


FIG. 17

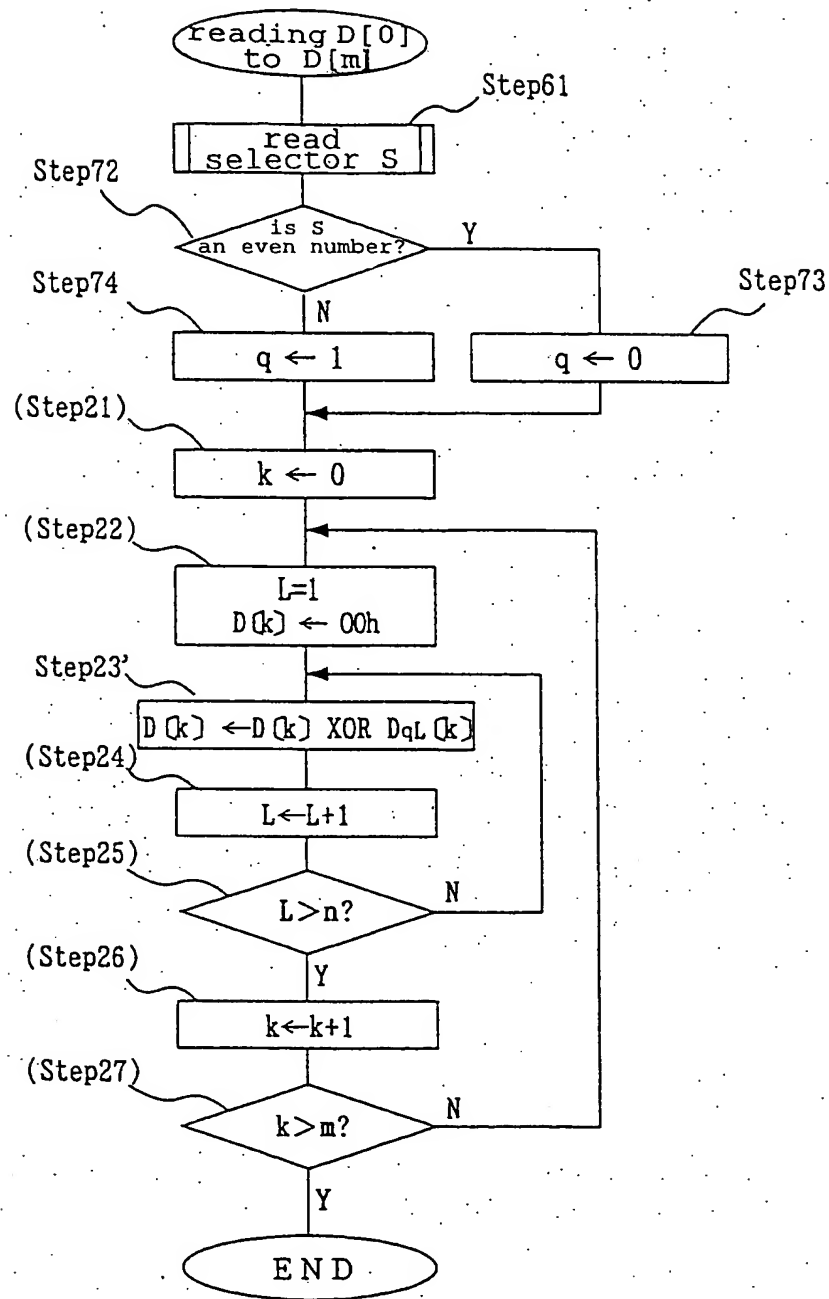


FIG. 18A

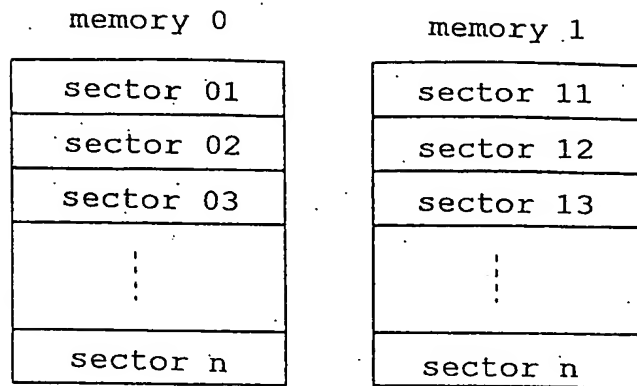


FIG. 18B

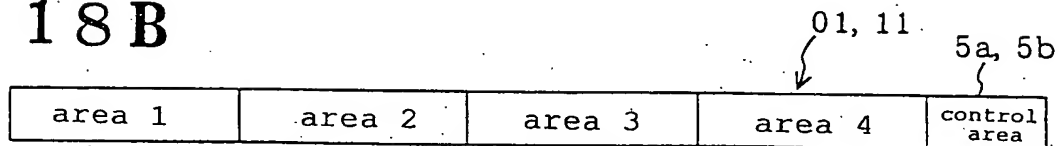
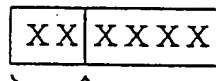


FIG. 18C

control area 5a, 5b



area selector

	write time	read time
0001	area 2	area 1
0011	area 3	area 2
0111	area 4	area 3
1111or0000	area 1	area 4

memory selector

FIG. 18D

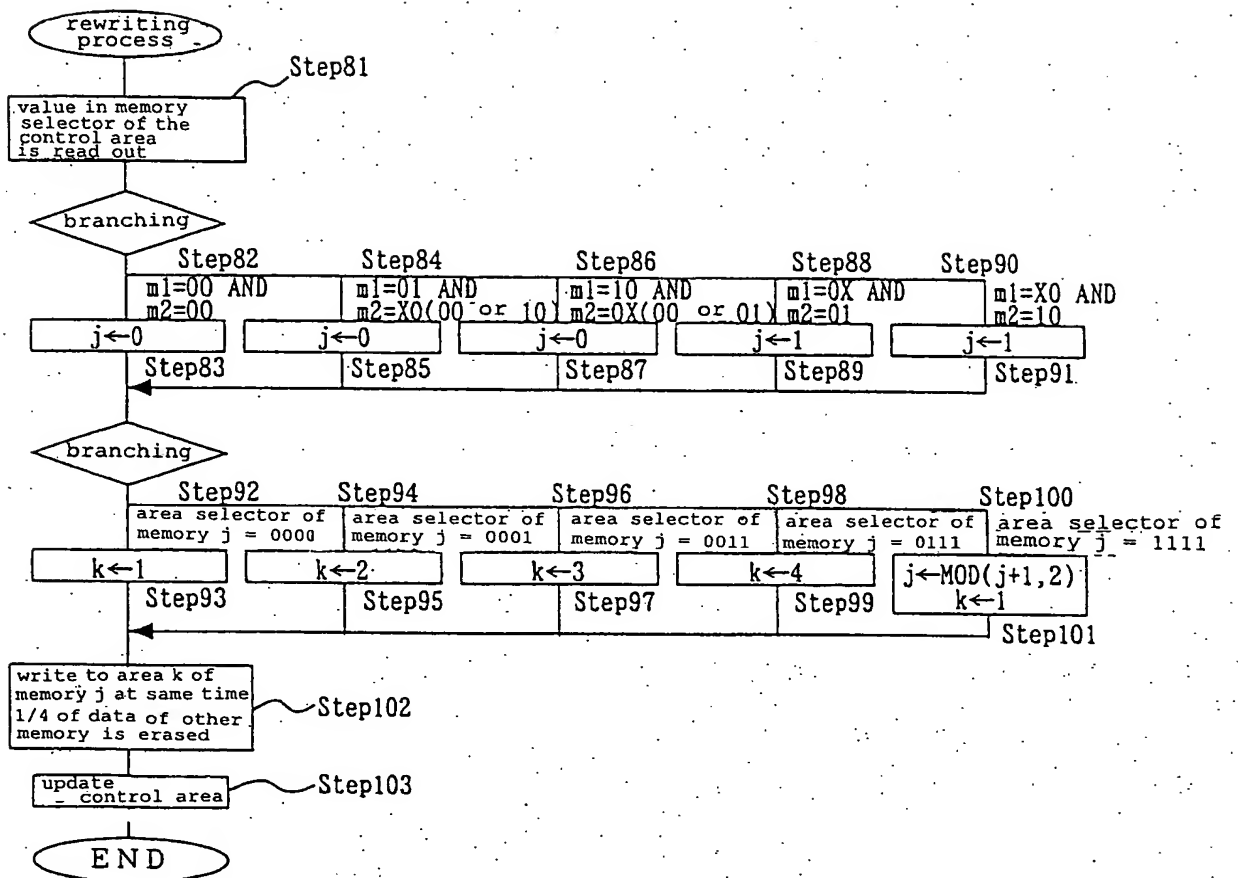
m0	m1	selected memory
00	00	memory 0
01	X0	memory 0
10	0X	memory 0
0X	01	memory 1
X0	10	memory 1

FIG. 18E

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FIG. 19



sector 01 of memory 0					sector 11 of memory 1				
control area 5a					control area 5b				
FIG. 20A	000000	All 00h	All 00h	All 00h	All 00h	All 00h	All 00h	All 00h	000000
FIG. 20B	010001	Data	All 00h	All 00h	All 00h	All 00h	All 00h	All 00h	000000
FIG. 20C	010011	Data	Data	All 00h	All 00h	All 00h	All 00h	All 00h	000000
FIG. 20D	010111	Data	Data	Data	All 00h	All 00h	All 00h	All 00h	000000
FIG. 20E	011111	Data	Data	Data	Data	All 00h	All 00h	All 00h	All 00h
FIG. 20F	0XXXXX	All XXh	All 00h	All XXh	All XXh	Data	All 00h	All 00h	All 00h
		:				:			
FIG. 20G	000000	All 00h	All 00h	All 00h	All 00h	Data	Data	Data	Data
FIG. 20H	100001	Data	All 00h	All 00h	All 00h	All XXh	All XXh	All XXh	All XXh
		:				:			
FIG. 20I	101111	Data	Data	Data	Data	All 00h	All 00h	All 00h	All 00h
FIG. 20J	X0XXXX	All XXh	All XXh	All XXh	All XXh	Data	All 00h	All 00h	All 00h
		:				:			
FIG. 20K	000000	All 00h	All 00h	All 00h	All 00h	Data	Data	Data	Data

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FIG. 21

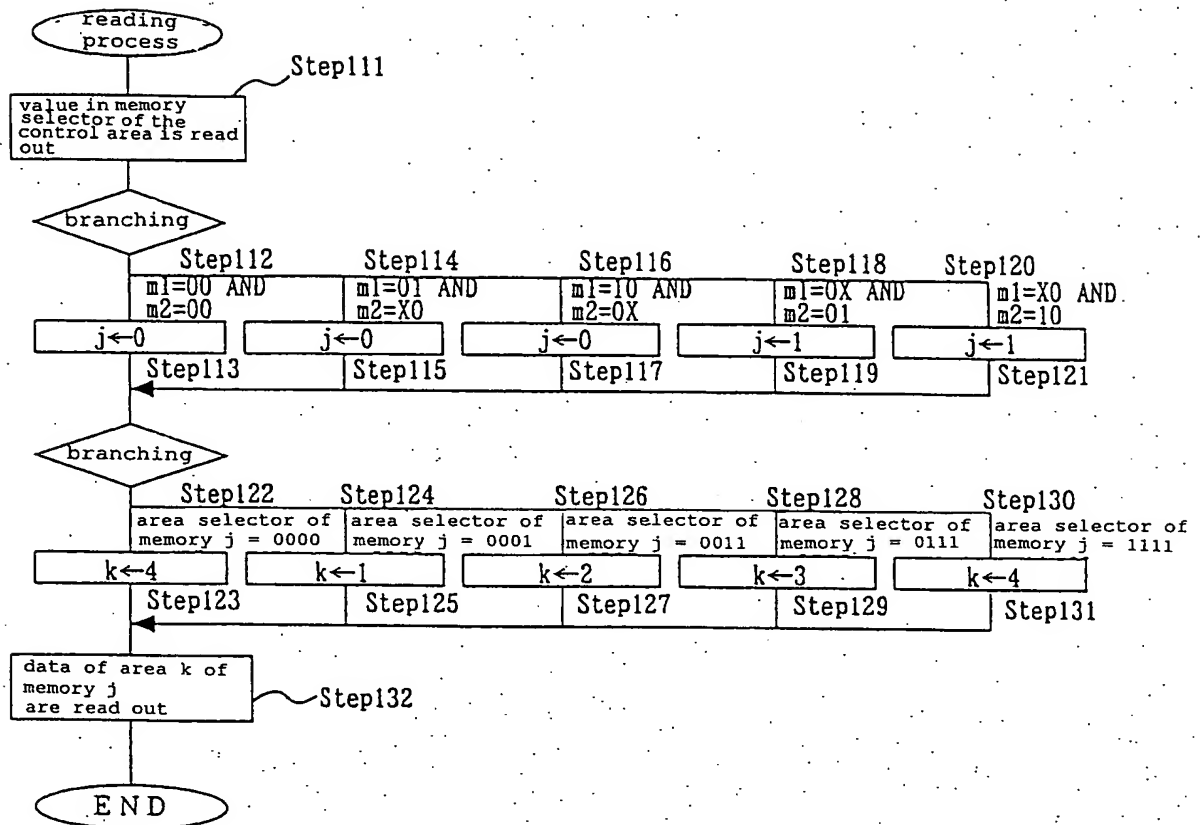


FIG. 22

